## Foreword

These proceedings contain the papers selected for presentation at the  $6^{\text{th}}$  Workshop on Cryptography and Security in Computing Systems (CS2 2019), held in Valencia, Spain, on January 21, 2019, in conjunction with the 14th High Performance and Embedded Architecture and Compilation (HiPEAC) conference.

Each anonymously submitted paper was reviewed by at least three members of the program committee, who did not have any conflict of interest with the authors of the paper, and was evaluated for significance, novelty, technical quality, and its potential to spark interesting discussions among attendees from the academic and industry research communities. The program committee's work was carried out electronically, and where needed, additional reviews were solicited. The final acceptance decision was made by consensus reaching consensus among the reviewers. Of the submitted papers, the program committee accepted 3 full papers and 2 short papers which were presented at the workshop.

A successful CS2 2019 was the result of the time and effort devoted by many individuals. We are thankful to the program committee and external reviewers for their work in evaluating several papers in a relatively short time-frame and their dedication to the quality of the program. We are also grateful to HiPEAC'19 General Chairs as well as to Workshops & Tutorials Chairs for their support in workshop organization and logistics.

Finally, we thank the authors of all submitted papers, as well as the workshop's attendees for contributing to the program and discussions surrounding. We hope you will find this program interesting and inspiring for your future research.

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