

A Physics-Based Compact Model of Stochastic Switching in Spin-Transfer Torque Magnetic Memory

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Abstract—Spin-transfer torque random-access memory (STT-RAM) is gaining momentum as a promising technology for high-density and embedded nonvolatile memory. Owing to random thermal fluctuations, switching transitions generally display statistical variations from cycle to cycle. Stochastic variations are critical to the hindering of memory and computing applications of STT-RAM. To enable the design of STT-RAM circuits for memory and computing, there is a need for accurate compact models capable of predicting the stochastic behavior. Here, we present a detailed model accounting for the anomalous thermal regime of switching deviating from the Néel–Brown thermal model below 200 ns. Anomalous switching is explained by the nonlinear lowering of the energy barrier associated with the perpendicular magnetic anisotropy (PMA). The model is extensively verified against the write-error-rate (WER) data as a function of applied voltage and pulselength and experimental switching time-delay distributions.

Index Terms—Magnetic tunnel junction (MTJ), spin-transfer torque magnetoresistive RAM (STT-MRAM), stochastic switching, switching variability modeling.

I. INTRODUCTION

S PIN-TRANSFER TORQUE random-access memory (STT-RAM) is attracting a strong interest because of the storage-class memory (SCM) [1]–[5], dynamic RAM (DRAM) replacement [6], and embedded nonvolatile memory [6]–[8] due to its fast switching [9], nonvolatile states, high endurance [10], CMOS compatibility, and low-current operation [11].

STT-RAM and spintronic devices in general find application also in novel non-von Neumann concepts of computing,

Manuscript received May 12, 2019; revised June 27, 2019 and July 29, 2019; accepted August 1, 2019. This work was supported by the European Research Council under Grant ERC-2014-CoG-648635-RESCUE. The review of this article was arranged by Editor J. Kang. (*Corresponding author: Daniele Ielmini*)

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Digital Object Identifier 10.1109/TED.2019.2933315

e.g., as an electronic synapse in neural networks [12], a nonvolatile logic [13], [14], and a random number generator (RNG) [15]. This boost of applicative interest in STT memory is due to the electronic switching that is crucial to enable high speed, negligible resistance variation, and high endurance [16].

STT magnetic memory is based on the magnetic tunnel junction (MTJ), which consists of a metal–insulator–metal structure comprising a thin MgO tunnel barrier ($t_{\text{MgO}} \approx 1 \text{ nm}$) separating two CoFeB ferromagnetic electrodes (FMs). Having a fixed magnetic polarization, one of these two electrodes is called pinned layer (PL), whereas the free layer (FL) can change its polarization between parallel (P) and antiparallel (AP) with respect to the PL. The MTJ resistance can assume two values depending on the relative orientation of the magnetic polarization of the FL and PL as a result of the tunnel magnetoresistance effect (TMR) [17]. In particular, the P state has a relatively low resistance R_P , while the AP state has a relatively high resistance R_{AP} . Electronic switching between the two states takes place by spin-transfer torque (STT), where the spin-polarized electrons flowing across the MTJ induce a change in the FL magnetic polarization by angular momentum conservation [18], [19]. In particular, perpendicular spin-transfer torque (p-STT), where the polarization of the two FMs is perpendicular to the MTJ plane, shows reduced switching current for the same retention time, thus enabling the low-power operation and improved scalability [20], [21].

In the thermal regime, STT switching takes place by random thermal fluctuations, thus featuring an intrinsically stochastic behavior. While the stochastic switching delay is harmful to the STT memory operation due to random switching variations, it is considered beneficial to true-RNG (TRNG) [15], stochastic computing [22], [23], and brain-inspired computing [24], [25].

Both memory and computing applications require accurate compact models for physics-based simulation of STT-RAM circuits. Previous STT switching models rely on the Landau–Lifshitz–Gilbert (LLG) equation [26], [27], but such numerical approaches are computing-intensive, which makes them unsuitable for electronic circuit simulators. Thus, simple analytical compact models are the ideal candidates for such tasks [28]. However, most analytical switching

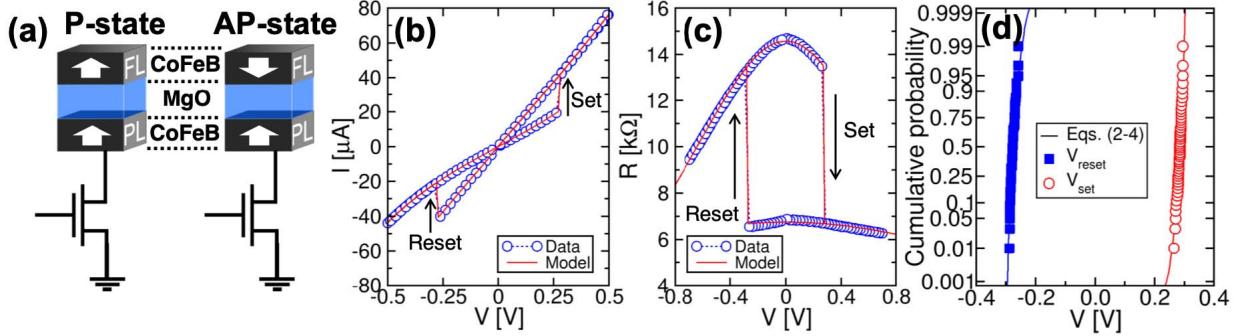


Fig. 1. (a) Perpendicular STT memory device: P state and AP state. (b) Measured I - V curve in dc conditions and (c) corresponding R - V characteristics evidencing set ($AP \rightarrow P$) and reset ($P \rightarrow AP$) transitions. (d) Distributions of stochastic switching voltages V_{set} and V_{reset} for 50 dc cycles.

models of STT-RAM are limited to the thermal regime (>200 ns) [29] and the precession regime (<1 ns) [30], while the intermediate regime is only introduced as a mathematical transition [31]–[33]. As a result, the physics involved in the switching behavior for the intermediate regime has not been conclusively addressed [34]–[37]. On the other hand, practical STT-RAM applications mostly work in the intermediate regime [38] due to the relatively large critical current and to prevent time-dependent dielectric breakdown [16].

While the physics of stochastic switching can be described by micromagnetic simulations [37], [39], most analytical approaches consist of empirical functional form fitting that can hardly provide a physical understanding [40], [41]. Moreover, statistical switching models are generally limited to few percentage [32], which is insufficient to predict the memory write error rate (WER) and the operation of stochastic computing primitives.

In this article, we present a physics-based stochastic model of STT memory. The compact model is capable of computing switching probability with 10^{-4} accuracy in the thermal regime (>200 ns) and the intermediate regime (<200 ns). The anomalous thermal regime of switching, deviating from the Néel–Brown thermal model, is attributed to nonlinear lowering of the energy barrier associated with the perpendicular magnetic anisotropy (PMA). The model accounts for WER data of 70 nm STT-RAM as a function of applied voltage for various pulsewidths, accurately reproducing switching-delay distributions.

II. P-STT SAMPLE CHARACTERISTICS

Fig. 1(a) shows the structure of STT magnetic memory devices used in this study, consisting of a CoFeB PL (bottom electrode, BE) and FL (top electrode, TE) with a crystalline MgO dielectric layer. The device is a p-STT-RAM with an out-of-plane magnetization easy axis with two stable states, namely, a P-state with low resistance and an AP-state with high resistance. The device cross-sectional area is $70\text{ nm} \times 70\text{ nm}$. **Fig. 1(b)** shows the measured current–voltage (I - V) characteristics under quasi-static ramped voltage (dc) conditions, where the set event, i.e., $AP \rightarrow P$ transition, occurred at a positive voltage $V_{set} = 0.27$ V. The reset event, i.e., $P \rightarrow AP$ transition, occurred at a negative voltage $|V_{reset}| = 0.27$ V,

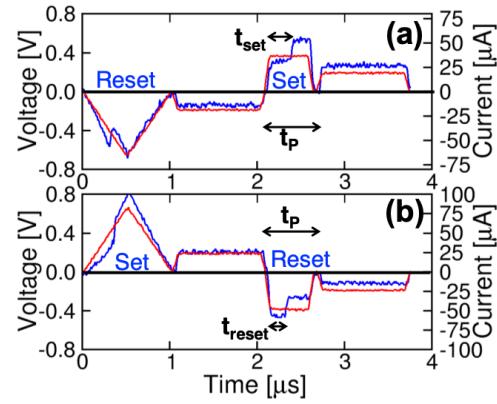


Fig. 2. Applied voltage waveform and corresponding read current traces during a WER experiment for (a) set and (b) reset transitions.

underlining the symmetric switching behavior of our samples. **Fig. 1(c)** shows the resistance–voltage (R - V) curve, where R was obtained from **Fig. 1(b)** as $R = V/I$. The figures also show the calculated conduction characteristics by an analytical model [16]. Cycle-to-cycle repetition of the switching characteristics shows statistical variation of V_{set} and V_{reset} . **Fig. 1(d)** shows the distributions of stochastic switching voltages V_{set} and V_{reset} for 50 dc cycles.

III. WER STUDY

To better address the cycle-to-cycle statistical variation of STT switching, we measured the WER (i.e., the failure rate of the switching transition) according to the technique in **Fig. 2**. For instance, **Fig. 2(a)** shows the voltage waveform applied to assess the WER of set transition, consisting of: 1) a negative-voltage triangular pulse at $V_- = -0.7$ V to deterministically initialize the cell in the AP state; 2) a negative-voltage square pulse for reading the cell state; 3) a positive-voltage square pulse, with amplitude V_A and duration t_P , to induce the stochastic set transition ($AP \rightarrow P$); and 4) a positive-voltage square pulse for a final reading to verify the cell state. Both read pulses have an amplitude $|V_{read}| = 0.15$ V, with the same polarity as the previous programming pulse to prevent read disturbs [42]. Pulses (1), (2), and (4) have the same pulsewidth of $1\text{ }\mu\text{s}$,

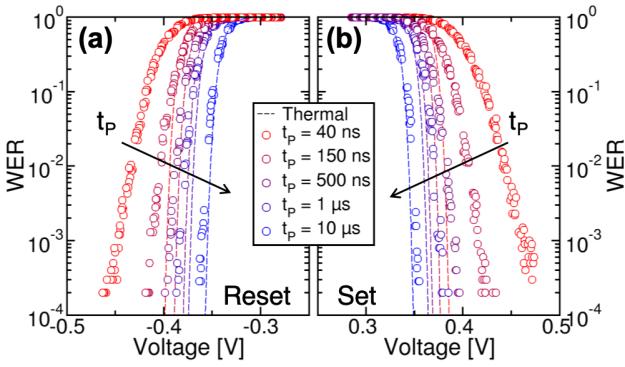


Fig. 3. Measured and calculated WER as a function of the applied voltage for different pulsewidths t_p for (a) reset and (b) set transitions. Data show a slope reduction for shorter t_p , not predicted by the thermal model described by (1) and (2).

while the set pulse has a variable pulsewidth t_p ranging from 40 ns to 10 μ s. The final state of the cell indicates the success or failure of the transition to P state. The WER can thus be defined as the ratio between the number of failures and the total number of cycles. Note that the current measured with a digital oscilloscope, also shown in Fig. 2(a), allows to assess the set time t_{set} in correspondence with the current rise during the set pulse.

Fig. 2(b) shows a similar waveform for the evaluation of reset WER, consisting of: 1) a positive-voltage triangular pulse of amplitude $V_+ = 0.7$ V to initialize the cell in P state; 2) a positive-voltage square pulse for reading the cell state; and 3) a negative-voltage square pulse with amplitude V_A and duration t_p to induce the stochastic reset transition (P \rightarrow AP); and (4) a negative-voltage square pulse for the final reading to assess the cell state. The final reading evidences the success or failure of the P \rightarrow AP transition, thus allowing to evaluate the WER as the ratio of failure events to the total cycles. The reset time t_{reset} can be estimated by the current trace in the figure. To characterize the WER, we repeated the waveforms of Fig. 2 10⁴ times for each value of t_p , except for $t_p = 10 \mu$ s, where only 7000 cycles were applied. All measurements were carried out at room temperature.

Fig. 3 shows the measured WER as a function of the voltage V applied during the stochastic square pulse for (a) reset transition and (b) set transition at increasing pulsewidth t_p . The WER drops almost exponentially at increasing voltage. As t_p decreases, the transition to low WER occurs at the increasing voltage and with decreasing slope. The same data of Fig. 3 are reported in Fig. 4 in a Weibull scale, namely, $\log(-\log(\text{WER}))$, for (a) reset transition and (b) set transition at increasing t_p . These results indicate that the set/reset voltages obey a Weibull distribution, at least in the thermal regime ($t_p > 200$ ns) where data appear as a straight line in the Weibull plot. As t_p decreases, the distributions indicate a marked deviation from the Weibull distribution [43]. Such deviation with respect to the thermal regime is consistent with the observation in [35] and [43] for $t_p < 600$ ns. A t_p -dependent WER slope, similar to our data in Fig. 3, was also reported in previous WER studies [44], [45], although no detailed physical explanation or modeling were given. A similar behavior, resulting in a

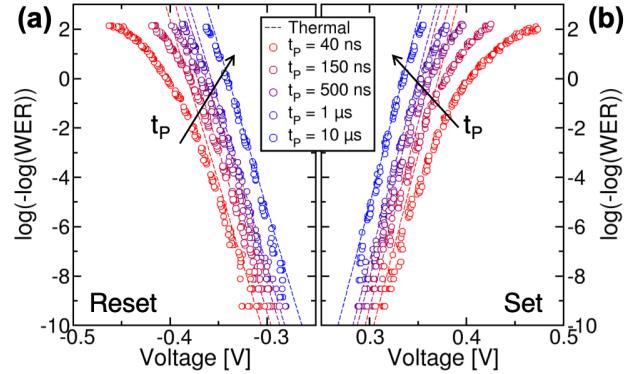


Fig. 4. Voltage distributions in a Weibull plot, i.e., $\log(-\log(\text{WER}))$, for (a) reset and (b) set transitions. Data show a marked deviation from the expected linear behavior in the thermal regime to an anomalous nonlinear shape for $t_p < 200$ ns.

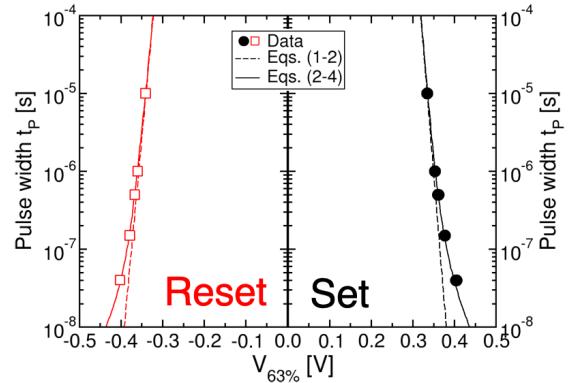


Fig. 5. Measured and calculated Weibull scale parameter $V_{63\%}$ for set and reset at different pulsewidths t_p .

larger than predicted WER at short t_p , was evidenced in the presence of back-hopping and low-probability bifurcated switching (LPBS) [46], [47]. However, thanks to our technique providing a real-time monitoring of the device current, we can rule out any possible back-hopping or LPBS in our measurements.

IV. MODELING OF STOCHASTIC SWITCHING

Data in Fig. 4 are summarized in Fig. 5, showing the Weibull scale parameter $V_{63\%}$, defined as the voltage for $\text{WER} = 63\%$, corresponding to $\log(-\log(\text{WER})) = 0$, as a function of t_p . The behavior of the transition voltage $V_{63\%}$ in Fig. 5 is usually explained by the thermal model in Fig. 6 describing the potential energy profile for the transition between P and AP states. The potential energy profile features two wells, corresponding to P and AP states, separated by an energy barrier E_A originating from the PMA in the MTJ. Referring to the AP \rightarrow P transition, the FL magnetic polarization experiences thermal fluctuations within the AP well, eventually inducing the transition across the energy barrier E_A , as shown in Fig. 6(a). By applying a voltage across the device, the spin-polarized current induces a spin-transfer torque on the FL causing an energy unbalance between the AP and P states, as shown Fig. 6(b) [48], [49]. Thus, the current-driven spin

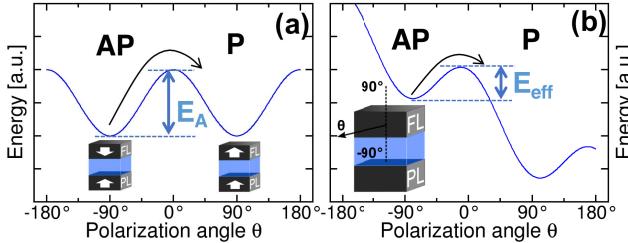


Fig. 6. Illustration of the PMA energy profile as a function of the FL magnetic polarization angle θ . Thermal fluctuations induce AP \rightarrow P transition across the energy barrier (a) with no applied voltage or (b) with positive applied voltage. The energy unbalance between AP and P states originates from the current-induced STT [48], [49].

TABLE I
SUMMARY OF SWITCHING MODEL PARAMETERS IN (1)–(4)

Parameter	Set	Reset
τ_0 [ns]	1	1
Δ	59.3	54
Δ'	84	84
V_{c0} [mV]	395	410
V_{c0}' [mV]	280	280

torque causes the lowering of the PMA barrier E_A , which accelerates the transition to the P-state [43]. Assuming a linear voltage-induced barrier lowering [43], [50], the characteristic switching time is given by

$$\tau_{\text{th}} = \tau_0 e^{\Delta \left(1 - \frac{V}{V_{c0}}\right)} \quad (1)$$

where $\Delta = E_A/kT$, while τ_0 and V_{c0} are constant [43]. The WER can thus be obtained by a Poissonian switching probability P given by

$$\frac{dP}{dt} = \frac{(1 - P)}{\tau} \quad (2)$$

where τ is the characteristic switching time equal to τ_{th} .

Equations (1) and (2) describe the thermally activated magnetization reversal of a single magnetic domain in the presence of spin-transfer torques. Here, the STT effect is included in the classical Néel–Brown formula [51] by introducing an effective activation energy $E_{\text{eff}} = E_A * (1 - V/V_{c0})$ to replace the true energy barrier E_A [29], [52]. Note that the same effect can be described by introducing an effective temperature $T_{\text{eff}} = T * (1 - V/V_{c0})^{-1}$ to replace the true lattice temperature [29], [52].

The calculations by (1) and (2) are reported in Figs. 3–5 using parameters from Table I. The calculation results indicate that the thermal model can account for the linear decrease of WER in the Weibull plot of Fig. 4 and the logarithmic decrease of $V_{63\%}$ in Fig. 5, both with slope Δ/V_{c0} , which controls the voltage-induced barrier lowering. However, the thermal model cannot explain the deviation from the linear behavior at $t_p < 200$ ns. To better understand the origin of the nonthermal behavior, we analyzed in Fig. 7 the Weibull shape factor, namely, the derivative of the Weibull distribution in Fig. 4 given by $d\log(-\log(\text{WER}))/dV$. The Weibull shape factor, which can be taken as a measure of the barrier lowering

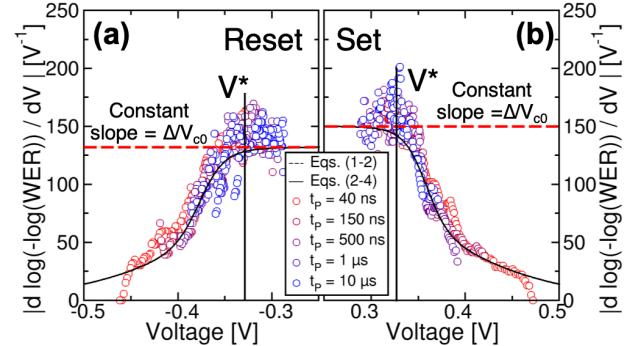


Fig. 7. Experimental and calculated Weibull shape factor $d\log(-\log(\text{WER}))/dV$ as a function of voltage, for both (a) reset and (b) set transitions, showing a drop in the barrier-lowering coefficient after a critical V^* , with respect to the thermal model value Δ/V_{c0} .

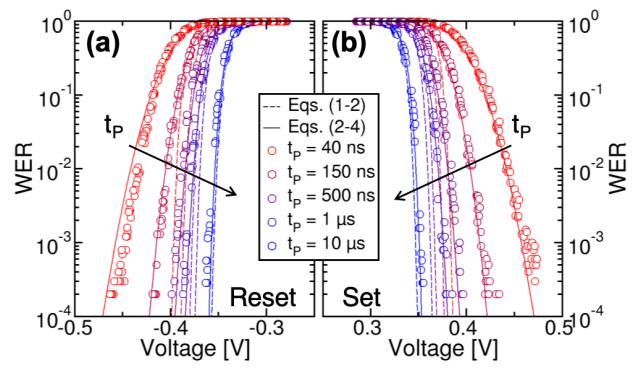


Fig. 8. Measured and calculated WER as a function of the applied voltage for different pulsewidths t_p for (a) reset and (b) set transitions. Calculations in (2)–(4) correctly account for the anomalous WER.

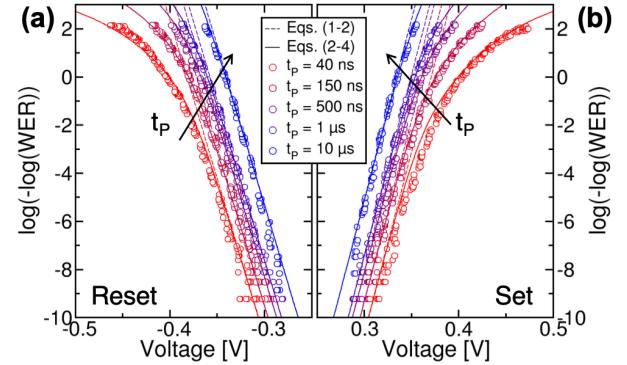


Fig. 9. Voltage distributions in a Weibull plot, i.e., $\log(-\log(\text{WER}))$, for (a) reset and (b) set transitions. The anomalous deviation for $t_p < 200$ ns is correctly described by (2)–(4).

coefficient, is plotted as a function of V , and compared with the ideal value Δ/V_{c0} from (1) and (2). Fig. 7 shows that the Weibull shape factor has a t_p -independent universal behavior, being close to Δ/V_{c0} at low V , then sharply decreasing above a critical voltage $V^* \sim 0.33$ V, which suggests a voltage-dependent barrier lowering coefficient. This is in stark contrast with respect to the thermal model predicting a V -independent barrier lowering coefficient Δ/V_{c0} .

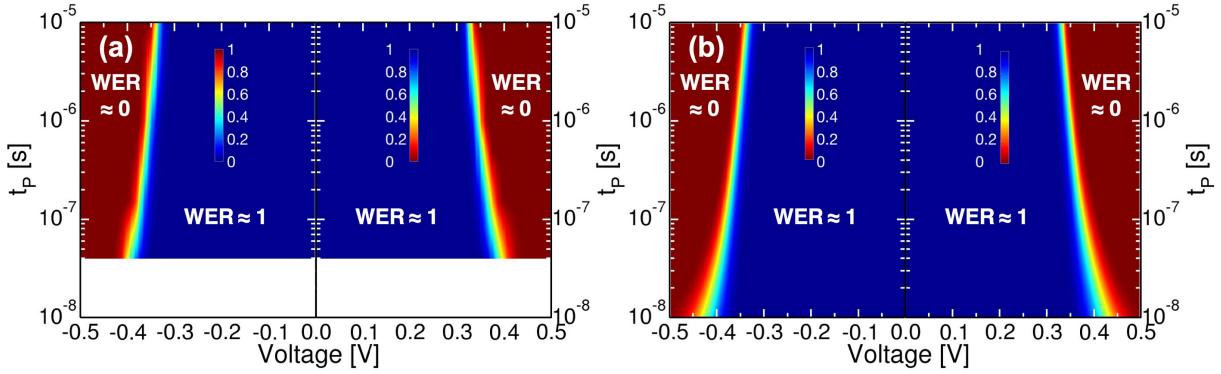


Fig. 10. (a) Experimental and (b) simulated Shmoo diagrams for STT switching. Color plots indicate WER (blue = high WER, red = low WER) for different pairs of applied voltage and pulselength.

To account for the anomalous barrier lowering at high voltage, we rewrote the characteristic switching time τ in (2) as

$$\tau = \tau_{\text{th}} + \tau'_{\text{th}} \quad (3)$$

where τ'_{th} is an additional characteristic time given by

$$\tau'_{\text{th}} = \tau_0 e^{\Delta' \left(1 - \text{erf} \left(\frac{V}{V_{c0}'} \right) \right)} \quad (4)$$

where parameters Δ' and V'_{c0} are given in Table I, and erf is the error function. The error function in (4) allowed to accurately describe the smooth step-like behavior of the Weibull shape factor in Fig. 7, correctly considering its voltage dependence. According to (3) and (4), the energy barrier lowering decreases at high V with respect to the thermal model, thus resulting in the STT transition slowing down compared with the thermal model.

The observed deviation from the thermal regime can thus be viewed as a nonlinear V -induced barrier lowering, similar to a reduced spin-transfer torque efficiency at high voltage [53]. Such effect cannot be neglected in order to accurately account for the intermediate regime between the thermal regime and the precession regime.

V. SIMULATION RESULTS

Simulation results by (2), (3), and (4) show excellent agreement with the experimental $V_{63\%}$ in Fig. 5 and the Weibull shape factor in Fig. 7, thus supporting the accuracy of our model in describing the anomalous deviation from the thermal model below 200 ns. Fig. 8 shows the measured and calculated WER, which is replotted in Fig. 9 in Weibull scale. Calculations by (2), (3), and (4) correctly describe the deviation at high voltage and relatively short t_p in the intermediate regime.

Fig. 10(a) and (b) shows the color plot of the measured WER and calculations by (2), (3), (4), respectively, as a function of V and t_p . Calculations well account for the nonlinear transition region between $\text{WER} = 1$ and $\text{WER} = 0$, which plays a crucial role in the design and operation of the STT-RAM device. Fig. 11 shows the Weibull distribution of measured and calculated t_{set} and t_{reset} in Fig. 2 for $t_p = 10 \mu\text{s}$ and increasing amplitude V . The transition times were

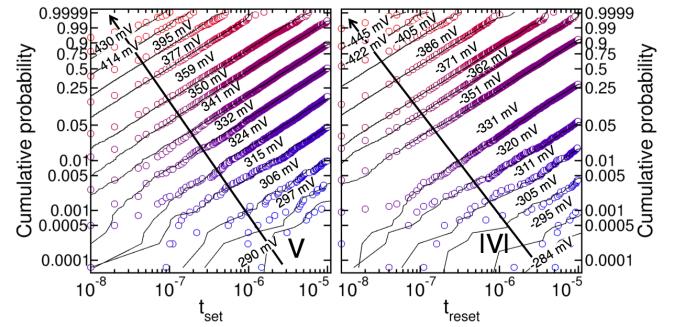


Fig. 11. Measured and calculated t_{set} and t_{reset} Weibull distributions for various values of applied amplitude V .

calculated by a Monte Carlo model of (2). The Monte Carlo simulations well account for both the slope and the voltage dependence of the distributions.

Combining the switching model with an analytical conduction model [16], quasi-static I - V and R - V curves can be simulated with their corresponding distributions of V_{set} and V_{reset} , as shown by calculations in Fig. 1.

VI. CONCLUSION

This article presents a new physics-based compact model for stochastic switching in STT-RAM. The model combines simplicity (three equations and five free parameters) and accuracy in computing WER, switching voltages, and switching times for both the thermal regime (> 200 ns) and the intermediate regime (< 200 ns) where deviation from the classical thermal-activated switching regime is observed. Thanks to its simple structure, the model features straightforward portability to other MTJ technologies and easy integration in commercial circuital simulators. The model can support the design of embedded STT memory and novel hardware-primitives for TRNG and stochastic computing.

ACKNOWLEDGMENT

The authors would like to thank W. Kula and W. Chen for discussions.

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