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High-quality graphene flakes exfoliated on a flat hydrophobic polymer

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ABSTRACT

We show that graphene supported on a hydrophobic and flat polymer surface results in flakes with extremely low doping and strain as assessed by their Raman spectroscopic characteristics. We exemplify this technique by micromechanical exfoliation of graphene on flat poly(methylmetacrylate) (PMMA) layers and demonstrate Raman peak intensity ratios I(2D)/I(G) approaching 10, similar to pristine freestanding graphene. We verify that these features are not an artifact of optical interference effects occurring at the substrate: they are similarly observed when varying the substrate thickness and are maintained when the environment of the graphene flake is completely changed, by encapsulating preselected flakes between hexagonal boron nitride. The exfoliation of clean, pristine graphene layers directly on flat polymer substrates enables high performance, supported and non-encapsulated graphene devices for flexible and transparent optoelectronic studies. We additionally show that the access to a clean and supported graphene source leads to high-quality van der Waals heterostructures and devices with reproducible carrier mobilities exceeding 50,000 cm²V⁻¹s⁻¹ at room temperature.

KEYWORDS Graphene; Hydrophobic substrate; flat PMMA; Raman; hexagonal boron nitride; van der Waals heterostructures; flexible electronics, transparent conductive materials

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Publishingraphene is typically prepared on oxidized highly doped silicon substrates, which enables the reliable excloliation of flakes¹, transfer of wafer-scale films², the optical identification of monolayers due to significant contrast enhancement for selected oxide thicknesses³, and allows its electrical characterization.^{4,5} Despite the possibility of maximizing yields and size of flakes by oxygen plasma pre-treatments on oxidized silicon¹, the direct contact with the SiO₂ layer can adversely affect the performance of graphene devices in several ways. First, the corrugation of graphene on SiO_2 induces the bending of sp^2 bonds in the basal plane of the monolayer, which is believed to lower the energy barrier for water and oxygen adsorption.^{6,7} Also, residual charged impurities arising from charge traps inside SiO₂, or water molecules adsorbed on the surface, can lead to additional unwanted doping and carrier scattering in graphene devices.^{6,7} Even in the absence of adsorbed molecules, polar optical phonons in SiO₂ impose an upper limit on the room temperature carrier mobility of around 40,000 cm²V⁻¹s^{-1,8} far away from intrinsic limits predicted for monolayer graphene of ~120,000 cm²V⁻¹s⁻¹. ⁹ In particular, typical graphene on SiO₂ devices exhibit a carrier mobility below 5,000 cm²V⁻¹s⁻¹,^{4,5,10} due to combinations of several of these effects. Raman spectroscopic measurements can be used to predict the impact of these effects on electronic properties prior to device fabrication: for graphene on SiO₂ a low intensity ratio of the 2D peak and G peak $I(2D)/I(G) \sim 1$ is observed; whereas pristine, suspended graphene shows I(2D)/I(G) ratios up to 10 (Ref. 11).

Strategies already exist for mitigating these detrimental factors, including transferring graphene onto hexagonal boron nitride (hBN) as a substrate to reduce the charged traps in the substrate, graphene corrugation and substrate-phonon scattering¹², or rendering the oxide surface hydrophobic through the use of self-assembled monolayers (SAMs) otherwise to avoid water contaminants.¹³⁻¹⁵ All these techniques result in increased electrical and optical performance, with measured carrier mobilities from 10,000 to 40,000 cm²V⁻¹s⁻¹ and I(2D)/I(G) ratios ~ **3**. We further note that some of these selected substrates already combine both features: hBN is not only flat but also hydrophobic¹⁶ and certain SAMs on SiO₂ substrates are hydrophobic and can be flatter than SiO₂¹⁶. The natural conclusion is that both the hydrophobicity and flatness of the substrate are prerequisites to obtain high-quality graphene. Both characteristics have a linked impact on the resulting doping and strain levels of graphene, and determine the limits for measured optical and electronic properties. The economical and scalable flat and hydrophobic substrates for the handling and production of graphene layers is therefore of paramount importance for the fabrication of high performance graphene-based devices, regardless of scale.

Here, we demonstrate the use of a flat, hydrophobic polymeric layer as an alternative graphene exfoliation substrate to SiO_2 in order to achieve high-quality flakes. We note that the exfoliation of graphene on polymer substrates has been already reported in literature, but without comparing the Raman spectral characteristics to the roughness of the polymer surface and the graphene quality.¹⁷⁻²⁰

We use PMMA which is hydrophobic and can show a roughness comparable to that observed for two dimensional materials supported by SiO_2 ,^{12,21} and smaller than bare SiO₂. Unless otherwise specified, a layer

Publishing hydrophilic polyvinyl acetate (PVA) is spun before spinning PMMA during preparation of the substrate. Graphene flakes exfoliated on these (PMMA/PVA) layers consistently display large I(2D)/I(G) ratios, approaching 10 in several cases, as well as showing other indicators of low residual doping and strain comparable to pristine, freestanding flakes. Importantly, similar Raman features are observed when varying the substrate thickness and composition by avoiding the usage of the PVA layer, and said Raman features are also maintained when these same flakes are transferred to a different environment by encapsulation between hexagonal boron nitride layers. Both these observations exclude the possibility that the high I(2D)/I(G) ratios seen are an artifact due to optical interference effects arising from the specific values of the thickness of the selected (PMMA/PVA) dielectric layers.²² Finally, we demonstrate the consistent observation of carrier mobilities over 50,000 cm²V⁻¹s⁻¹ in hBN encapsulated devices when using graphene originally exfoliated on PMMA polymer. These mobilites are above the average of those reported for an ensemble²³ of fully hBN encapsulated devices which used SiO₂ as the initial exfoliation substrate^{23,24} and make possible a more reproducible fabrication of high mobility encapsulated graphene van der Waals heterostructures.

> The polymeric substrate is fabricated as follows: PVA (15% dissolved in deionized water) is spun at 2000 rpm on 500 µm Si / 300 nm SiO₂ thermally oxidized substrates, and baked out at 100°C for 2 minutes. PMMA (4% dissolved in chlorobenzene) is spun on top at 2000 rpm and baked at 180°C for 2 minutes. We emphasize the critical importance of the polymer concentration, spin coating speed and solvent in order to achieve flat PMMA films.^{25,26} Graphene is then mechanically exfoliated on top of PMMA and initially identified by optical contrast (Supplementary Material). Atomic force microscopy (AFM, Bruker Innova, tapping mode, equipped with an AFM cantilever having a characteristic spring constant of k = 40 N/m is used to measure the roughness of the flakes and substrates.^{7,12,27} Figure 1a shows an AFM image of the PMMA surface after coating PMMA/PVA polymers on Si/SiO₂. The measured surface roughness (given by the standard deviation of a fitted Gaussian) of graphene flakes exfoliated on top of these polymer layers on $1\mu m^2$ scan windows is 0.16 ± 0.03 nm (Figure 1b). These values are similar to those observed for graphene exfoliated on hBN (roughness ~0.1 nm)¹² and lower than those of graphene exfoliated on SiO₂ (0.22 \pm 0.04 nm, Figure 1b; in agreement with Ref.12). We note that the black dots in Figure 1a are pinholes in the PMMA layer, which are commonly observed after spin-coating and baking this polymer.^{28,29} Graphene on these pinholes is locally suspended and indented during the AFM measurements since our tip is specifically selected to measure supported flakes. The presence of these pinholes, however, does not affect the conclusions of Figure 1b: pinholes increase the measured surface roughness of graphene flakes on PMMA as compared to regions between pinholes, but the overall roughness with pinholes included is still smaller than the roughness of graphene on SiO₂.

> Raman spectroscopy³⁰ (Thermo Scientific DXR, 455 nm excitation, 50x objective, $\sim 1 \mu m^2$ spot size) is used to assess the quality of more than 50 graphene flakes exfoliated on PMMA/PVA in terms of strain, doping and defects in the graphene layers,^{11,27,30} by inspection of the peak intensity ratio, I(2D)/I(G), as well as the full-width-at-half-maximum (FWHM) and position (Pos) variations of the G and 2D peaks. The Raman



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We categorise more than 50 measured monolayer graphene flakes exfoliated on PMMA/PVA according to their average I(2D)/I(G) ratios, (Figure 2a): I(2D)/I(G) < 4 (white dots), 4 < I(2D)/I(G) < 8 (grey dots), I(2D)/I(G) > 8 (black dots). More than 60% of the studied graphene flakes have a I(2D)/I(G) ratio > 4, and more than 30% have ratios I(2D)/I(G) > 8. By comparison, graphene on SiO₂ only shows $I(2D)/I(G) < 2^{27,30}$ and $I(2D)/I(G) \le 4$ on hydrophobized SiO₂³² and on hBN³¹. Figure 2a shows the correlation between the 2D and G peak positions of these flakes (Pos(2D) vs Pos(G)), commonly used to separate and obtain specific information about strain and doping levels existent in non-defected graphene³³⁻³⁵. Dashed lines in the figure represent the predicted evolution of the Raman parameters in the unique presence of one of the mechanisms, i.e. strain (grey dotted line) or doping (black dashed line). These lines intersect at $Pos(G)^0=1582$ cm⁻¹, Pos(2D)⁰=2700 cm⁻¹, which corresponds to pristine (strain-free, doping-free) graphene flakes.³⁶ We note that while the value of the G peak position $Pos(G)^0=1582$ cm⁻¹ is well-established for clean, freestanding or encapsulated graphene,^{11,21} the exact position of the 2D peak is subject to discussion.³⁶⁻³⁸ Small deviations in Pos(2D)⁰ occur due to the subtle interplay between the phononic and electronic energy dispersions³⁷, which ultimately depend on the surrounding environment.^{36,38} For our purposes, we adopt the calculated Pos(2D)⁰=2700 cm⁻¹ as the value in pristine graphene for our laser excitation energy (2.72 eV).^{36,38} With these considerations, we can confirm the small variations between the G peak positions $\Delta Pos(G) = (Pos(G))$ $Pos(G)^0 \le 5cm^{-1}$ and 2D peak positions $\Delta Pos(2D) = (Pos(2D) - Pos(2D)^0) \le 13cm^{-1}$ for all the flakes under study with respect to the pristine case $(Pos(G)^0, Pos(2D)^0)$. Furthermore, flakes on PMMA/PVA show a fullwidth-at-half-maximum of the 2D peak (FWHM(2D)) lying between 22 and 28 cm⁻¹ (Figure 2b), also similar to pristine, freestanding graphene.¹¹ Quantitative values of strain and doping can be calculated from these Raman signals³⁶⁻³⁸. Specifically, in our flakes with ratios I(2D)/I(G)>8, we extract averaged strain levels of -0.17% and doping levels below 2.5.10¹² cm⁻¹ (Supplementary Material). These values are well below strain and doping values estimated for graphene on $SiO_2^{27,36-38}$, typically larger than 0.2% and 5.10¹² cm⁻¹ for strain and doping respectively, in accordance to the expectations from the higher I(2D)/I(G) ratio of these flakes.

Thus, our combined AFM and Raman data indicate that dry and flat PMMA itself is harmless to graphene. Instead, the associated usage of solvents^{19,20}, heat treatments^{6,7} and/or substrate roughness^{7,21} are responsible of the degradation of the optical and electronic properties of this two-dimensional material.



Publishing Publishing of the eposibility that the high I(2D)/I(G) ratios appearing on PMMA/PVA arise incidentally from interference effects caused by the selected dielectric layers,²², we have confirmed that large Raman I(2D)/I(G) ratios are also observed when changing the composition and thickness of the substrate by eliminating the PVA layer (Supplementary Material), i.e. when supported purely by the ~200nm thick PMMA layer. We have additionally measured the Raman characteristics of selected graphene flakes before and after encapsulation with hBN^{23,24}, and have confirmed that the high I(2D)/I(G) ratios are conserved after this complete change of the environment of the graphene. For this task, commercially available hexagonal boron nitride crystals (HQ Graphene) are exfoliated on oxidized silicon substrates (300 nm of SiO₂ thermally grown on top of highly-doped silicon) and are used as encapsulation for heterostructure devices. In assembling heterostructures we aim to maintain the low levels of strain and doping observed in the graphene on PMMA samples. We largely follow the assembly methods presented in Ref.23 using polypropylene carbonate (PPC) as a transfer polymer to pick up and drop down hBN and graphene but with some modifications.

> We observe by AFM that exposing graphene exfoliated on PMMA to temperatures above 70°C has a detrimental effect on the roughness, with the surface roughness increasing above 0.4 nm at 100°C. Such temperatures are unnecessary in the assembly presented here however, as the graphene flakes are already clean and dry on the hydrophobic PMMA. By performing assembly at 60°C, just over the glass transition temperature of the PPC, the drop down of an hBN flake on top of graphene can be accomplished without increasing the roughness. After the hBN flake has been fully dropped down on top of graphene, the partial heterostructure can safely be exposed to a temperature ramp from 60°C to 100°C over 5 minutes to ensure adhesion between the layers. The two flakes are then lifted together and dropped down on top of the bottom hBN (Figure 3b, lower panel) at 110° C, similar to Ref.23, to avoid strain in the final heterostructure. We note that it is possible to perform this last "drop-down" step at 110°C since graphene supported by hBN will not corrugate at this temperature. Raman maps demonstrate the lack of strain and doping introduced to the graphene by this modified encapsulation process when using pristine, preselected graphene flakes on PMMA/PVA substrates. In particular, Figure 3d shows a homogeneous encapsulated flake with I(2D)/I(G) ratios higher than 8 and Pos(G) around 1582 cm⁻¹, similar to the ratio of the graphene flake on PMMA/PVA prior to its encapsulation (Supplementary Material). Additional data on the evolution of the Raman features of graphene from polymer substrates before and after encapsulation with hBN is given in the Supplementary Material.

> Subsequently to the fabrication of encapsulated samples and the measurement of the Raman spectral characteristics before and after this process we fabricate electrical devices for measurement of the corresponding carrier mobility values. Although it would be highly desirable to verify that graphene flakes on flat PMMA have a high mobility –i.e. without resorting to additional hBN encapsulation steps-, we note that this is not trivial since PMMA is not compatible with standard electron beam lithography based fabrication steps typically used to contact graphene. Alternative contacting methods such as shadow masks⁷



Publishing lithography-free microprobing techniques³⁹ are difficult to apply here due to reduced flake sizes obtained (most of them around 10 μ m × 10 μ m, see Supplementary Material). State-of-the-art nanoprobing techniques⁴⁰ could be a possible way to measure the mobility of these flakes.

Based on the assumption that the mobility values obtained for a graphene flake are correlated with the Raman spectral properties and in order to give an indicative mobility value that might be expected from the polymer supported flakes here described, we have measured the mobility of hBN encapsulated devices produced from PMMA exfoliated and preselected high I(2D)/I(G) ratio graphene flakes. To undertake these measurements, we define 5 μ m x 5 μ m square-shaped regions and add electrical contacts using standard fabrication techniques.^{23,24} The homogeneity of the samples was first investigated by comparing the resistance *R* in two perpendicular configurations (see Supplementary Material). Figure 3d shows the room temperature resistivity ρ and field-effect mobility μ as a function of the back-gate voltage V_g for one of the fabricated devices. These parameters are calculated from $\rho = \frac{\pi}{\ln 2}R$ (resistivity calculation with van der

Pauw configuration for square geometries²⁴) and $\mu = \frac{t}{\varepsilon} \frac{\partial \sigma}{\partial V_g}$, respectively, where ε is the dielectric permittivity, t is the dielectric thickness (300nm SiO₂, 10 nm hBN) and $\sigma = 1/\rho$ is the electrical

conductivity of the device. Carrier mobilities above 50,000 cm²V⁻¹s⁻¹ were measured for both electron and holes (Figure 3d) in the two examined devices at room temperature.

In conclusion, we have shown that exfoliating graphene on a hydrophobic, flat polymeric substrate leads to very low residual strain, doping and roughness as compared to the same operation on SiO₂. Whilst such flakes hold promise for electronic and optical studies – particularly those where a transparent and flexible substrate is required - without substantial further processing, we have additionally shown that these graphene flakes are ideally suited for the reproducible fabrication of encapsulated van der Waals heterostructures with mobilities above 50,000 cm²V⁻¹s⁻¹. We anticipate that the scheme presented for reducing strain and doping in graphene is applicable to two-dimensional materials in general will enable the fabrication of related heterostructures with properties that more consistently approach theoretical limits. From a practical perspective, Ratnan spectroscopy is ideal for rapidly prescreening graphene flakes, suggesting that such an approach can be integrated⁴¹ into automated systems for the assembly of van der Waals heterostructures in order to avoid producing heterostructures with non-idealities. Furthermore, the ability to exfoliate graphene on flat PMMA not only reduces the effects of strain and doping on the graphene - resulting in high carrier mobilities - but also provides for a much reduced cost of substrate with arbitrary size as compared to oxidized silicon.

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See supplementary material containing an optical image of graphene on PMMA/PVA, the evolution of Raman spectra of graphene flakes on PMMA/PVA and after hBN encapsulation and electrical measurements verifying the homogeneity of hBN encapsulated graphene devices, quantitative estimation of strain and doping levels on graphene flakes on PMMA/PVA, Raman spectra of graphene on PMMA/SiO₂ and surface roughness of spin-coated PVA films.

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Figure 1. Characterization of graphene flakes on PMMA/PVA. (a) AFM image of the monolayer graphene; the mapped area covers both a graphene flake and bare PMMA surface. Step size in the XY plane is 20 nm. Black dots in the image are pinholes, features commonly appearing in spin-coated PMMA films^{28,29} where graphene is locally suspended (b) Histogram of the height distribution (surface roughness) measured for graphene on SiO₂ (black squares) and for graphene on PMMA/PVA (red circles) by AFM on $1\mu m^2$ scan windows. Solid lines are Gaussian fits to the distributions. (c) Raman spectra of single, bi- and trilayer graphene normalized by the G peak intensities. The spectra are collected on PMMA/PVA-supported flakes and show a higher I(2D)/I(G) ratio as compared to usual values obtained on SiO₂ supported graphene.





Figure 2. Raman analysis of PMMA-supported single layer graphene. Data points are categorized by I(2D)/I(G) as indicated. (a) Correlation between the G and 2D Raman peaks frequencies. Data points are from single point spectra of >50 different single layer flakes. The predicted evolution³³⁻³⁵ in the unique presence of strain (grey dotted line) or doping (black dashed line) are also plotted, and intersect at a point (asterisk) representing doping- and strain-free graphene (Pos(G)⁰, Pos(2D)⁰). Dispersion of the data around this intersection is very small – with Pos(G) +/- ~5cm⁻¹ and Pos(2D) +/- ~7 cm⁻¹ showing both a low level of doping and strain in the exfoliated flakes and low variation in these quantities. (b) Correlation between Pos(2D) and FWHM(2D) for the PMMA-supported exfoliated single layer graphene.





Figure 3. Stacking method and samples (a-c) Schematic illustration of the developed stacking procedure, which is partially following the method published in Ref.23, used to realize hBN/SLG/hBN van der Waals heterostructures. (d) Raman map showing the I(2D)/I(G) ratio of a representative encapsulated graphene flake. Scale bar is 5µm (e) Histogram of I(2D)/I(G) ratios for the squared indicated region of 5µm side in (d). (f) Resistivity and mobility of the device. Inset: SEM micrograph of the measured device. Scale bar is 5µm



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