Abstract—This paper deals with the design of sliding mode based droop control strategies for parallel-connected inverters in railway vehicles. Indeed, the presence of auxiliary devices, which can be connected and disconnected at any time instant, makes the introduction of parallel modules an efficient solution. Among the possible techniques, droop control represents an efficient and easy-to-implement approach. However, each inverter is affected by load variations, nonlinearities and unavoidable modelling uncertainties, thus making the use of sliding mode controllers perfectly adequate for this kind of application. More specifically, relying on a sliding surface designed on the basis of a voltage-current droop characteristic, two second order sliding mode (SOSM) control algorithms, belonging to the class of Super-Twisting and Suboptimal SOSM control, are proposed.

I. INTRODUCTION

Nowadays, railway vehicles are considered by far the most efficient means of transportation from the point of view of energy consumption and therefore this is a strategic sector in the transportation industry [1], [2]. The need to decrease energy consumption in order to be sustainable and more profitable in the future has paved the way for new control methods and architectures for trains. These methods differ on the basis of certain factors: some of them are operator based, which require upgrade in the vehicle technology, while other techniques are control based [3]. For example, in order to reduce energy, an operator can deploy more valuable rolling stock by using more energy efficient engines or streamlining. Also, the operator may work to better match the capacities of the trains with the demand and deploy measures concerning heating, cooling, lightning and other auxiliary services.

As for auxiliary devices, they are typically supplied by inverters located inside the train carriages and a widely spread configuration relies on a centralized control architecture. However, this scheme has the problem to be not very efficient in the low power range and the Voltage Source Converters (VSCs) can be often expensive and bulky. Then, a valid alternative is that of using a decentralized architecture with parallel-connected inverters, each one of them is capable to manage a fraction of the full power [4], [5]. The parallel connection of inverters has indeed the main advantages of modularity and flexibility, allowing disconnection of some units on the basis of the required power level. As a consequence this architecture is more efficient in case of low power range and implies lower volume, weight and costs.

In the literature the so-called droop control is considered an effective decentralized strategy to cope with this type of systems [6]–[8]. More specifically, the parallel architecture is typical of islanded microgrids where power converters have to satisfy power requirements while fulfilling voltage stability and frequency constraints. The scope of droop control is to make multi-inverters share loads according to their power rating. In [9], for instance, the inverter output characteristic is changed relying on a virtual impedance control and active and reactive power-voltage droop functions are employed. Further improvements are introduced in [4] and then in [10], where voltage-current droop control under synchronous reference frame is proposed, with advantages in terms of computational burden and decoupling of voltages and currents. Typically, Proportional-Integral (PI) controllers are adopted to regulate the system, while other solutions, always applied for microgrids case studies, are related to consensus algorithms or optimal control strategies, as reported in [11]–[13].

Since the presence of VSCs can produce undesired harmonics and the presence of coupling effects and modelling uncertainties is unavoidable, robust control strategies are essential for this type of applications. Among the possibilities, Sliding Mode Control (SMC) is a powerful solution, which perfectly fits the control problem to solve [14]. Relying on a discontinuous control action, generated on the basis of the so-called sliding variable typically related to the required performance of the plant, interesting robustness features of the controlled system can be achieved. If on one hand the main drawback is the so-called chattering phenomenon, on the other hand Higher-Order Sliding Mode (HOSM) controllers are a valid solution to alleviate it by confining the discontinuity of the control law into the derivative of the real control input [15]–[18]. In this paper, inspired by [10], a sliding variable, which relies on a voltage-current droop characteristic, is designed in order to enable the current sharing objective, and two second-order SMCs of Super-Twisting and Suboptimal type are introduced to solve the problem at hand.
The paper is organized as follows. After some preliminaries on the train structure and the operative mode in Section II, the model is introduced in Section III. The proposed algorithms are presented in Section IV, while simulations, carried out on a realistic setup, are illustrated in Section V. In Section VI some conclusions are gathered.

II. TRAIN ARCHITECTURE

In this section, for the readers’ convenience, the architecture of a parallel multi-inverters system is illustrated.

Let us denote as $\ell_i$, $i = 1, \ldots, n$ the auxiliary loads, fed by parallel voltage source converters indicated as VSCi. The parallel configuration allows one to achieve more flexibility in order to add or remove elements from time to time. Figure 1 illustrates in detail the schematic single-line diagram of a system composed, for the sake of simplicity, of three parallel-connected inverters. The basic element of each inverter subsystem is given by a direct current (dc) voltage source $V_{DCi}$. The interfacer is interlinked with the rest of the grid through three components: the VSC, a filter and a capacitor. Typically the VSC is a pulse with modulation (PWM) converter, which transforms the dc to alternate current (ac). On the other hand, the resistive-inductive filter $R_i L_i$ extracts the fundamental frequency of the VSC output voltage. The capacitor $C_i$ determines instead the output voltage of the $i$th subsystem indicated with $v_{C_i}[i]$, as well as the corresponding output current, namely $i_C[i]$. Each subsystems is then connected to a local three-phase parallel resistive-inductive $R_{o[i]} L_{o[i]}$ load, and, apart from the last one, they are connected each other through a resistive-inductive $R_{\ell[i]} L_{\ell[i]}$ line impedance.

III. PROBLEM FORMULATION

In this section, the model of the train parallel-inverters is described and the control problem is introduced.

A. Train inverters model

Consider now Figure 1 where three parallel-connected inverters are illustrated. Applying the Kirchhoff’s voltage and current laws, in the so-called abc-frame, the model is

$$\begin{align*}
\frac{dv_{C_1}}{dt} &= \frac{1}{L_1} \left( i_{t_1}^{[1]} - v_{C_1}[i] - R_{t_1}[i] \right), \\
\frac{dv_{C_2}}{dt} &= \frac{1}{L_2} \left( i_{t_2}^{[2]} - v_{C_2}[i] - R_{t_2}[i] \right), \\
\frac{dv_{C_3}}{dt} &= \frac{1}{L_3} \left( i_{t_3}^{[3]} - v_{C_3}[i] - R_{t_3}[i] \right),
\end{align*}$$

where $i_{t_1}^{[1]}, i_{t_2}^{[2]}, i_{t_3}^{[3]}$, $v_{C_1}[i], v_{C_2}[i], v_{C_3}[i]$ are $3 \times 1$ column vectors (one entry for each phase) representing the VSC output current, the load current, the line current, the VSC output voltage and the capacitor voltage, respectively. The inverter output current of interest is instead given by

$$i_0[i] = i_{t_1}^{[1]} - i_{t_3}^{[3]}.$$  

Note that the dependence of all the variables on time $t$ is omitted, for the sake of simplicity.

In order to achieve a state model of system (1) in a suitable form for control design, each three-phase variable $s \in \{i_{t_1}^{[1]}, i_{t_2}^{[2]}, i_{t_3}^{[3]}, v_{C_1}[i], v_{C_2}[i], v_{C_3}[i]\}$ can be transferred to the synchronous rotating dq-frame, with $S \in \{I_{d1}^{[1]}, I_{d2}^{[2]}, I_{d3}^{[3]}, V_{d1}^{[1]}, V_{d2}^{[2]}, V_{d3}^{[3]}\}$ being the dq variables, by using the Clarke’s and Park’s transformations. Hence, the so-called state-space model of (1)–(2), extended to $n$ parallel-connected inverters, is

$$\begin{align*}
\dot{x}[i] &= Ax[i] + Bu[i] + M \left( w[i] - w[i+1] + d[i] \right), \\
y[i] &= C_1 x[i], \\
\dot{w}[i] &= E[i] w[i] + G[i] C_2 \left( x[i] - x[i-1] \right), \\
d[i] &= F[i] d[i] + H[i] C_2 x[i],
\end{align*}$$

where $i = 1, \ldots, n$, with $w[n+1] = 0$ and $x[0] = 0$,

$$x[i] = \begin{bmatrix} I_{d1}^{[i]} \\ I_{d2}^{[i]} \\ I_{d3}^{[i]} \\ V_{d1}^{[i]} \\ V_{d2}^{[i]} \\ V_{d3}^{[i]} \end{bmatrix}, \quad u[i] = \begin{bmatrix} V_{q1}^{[i]} \\ V_{q2}^{[i]} \end{bmatrix}, \quad d[i] = \begin{bmatrix} d_{t_1}^{[i]} \\ d_{t_2}^{[i]} \\ d_{t_3}^{[i]} \end{bmatrix}, \quad w[i] = \begin{bmatrix} I_{q1}^{[i]} \\ I_{q2}^{[i]} \end{bmatrix}.$$  

\[ (4) \]
while the matrices are
\[
A = \begin{bmatrix}
-\frac{R_0}{L} & 0 & -\frac{1}{L} & 0 \\
-\omega_0 & -\frac{R_0}{L} & 0 & -\frac{1}{L} \\
\frac{1}{C} & -\omega_0 & 0 & 0 \\
0 & \frac{1}{C} & -\omega_0 & 0 \\
\end{bmatrix}, \quad B = \begin{bmatrix}
\frac{1}{L} & 0 \\
0 & \frac{1}{L} \\
0 & 0 \\
0 & 0 \\
\end{bmatrix},
\]
\[
C_1 = \begin{bmatrix}
1 & 0 & 0 & \omega_0 C_t \\
0 & 0 & 1 & -\omega_0 C_t \\
\end{bmatrix}, \\
C_2 = \begin{bmatrix}
0 & 0 & 1 & 0 \\
0 & 0 & 0 & 1 \\
\end{bmatrix}, \\
M = \begin{bmatrix}
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
-\frac{1}{C_t} & 0 & -\frac{1}{C_t} & 0 \\
\end{bmatrix}, \\
F[i] = \begin{bmatrix}
-\frac{R[i]}{L[i]} & 0 \\
-\omega_0 & -\frac{R[i]}{L[i]} \\
\end{bmatrix}, \\
H[i] = \begin{bmatrix}
\frac{1}{L[i]} & 0 \\
0 & \frac{1}{L[i]} \\
\end{bmatrix}, \\
E[i] = \begin{bmatrix}
-\frac{R[i]}{L[i]} & 0 \\
-\omega_0 & -\frac{R[i]}{L[i]} \\
\end{bmatrix}, \\
G[i] = \begin{bmatrix}
0 & 0 \\
0 & \frac{1}{L[i]} \\
\end{bmatrix}.
\]
(5)

It can be observed that, while matrices \(A, B, C_1, C_2\) and \(M\) are independent of the \(i\)th VSC, matrices \(F[i], H[i], E[i]\) and \(G[i]\) depend on the parameters of the 4th load and on those of the line between the \(i\)th and \((i+1)\)th inverter \((l_{i,i+1})\). Furthermore, the vector \(d[i]\) can be considered as a disturbance term for which the following assumption holds.

**Assumption 1:** All the state variables are measurable and the current loads \(d[i]\) are unknown and bounded, of class \(C\) and Lipschitz continuous.

As for the \(R[i]L[i]\) load, having in mind the field implementation of the proposal, it is typically expressed in terms of apparent power and power factor, namely \(\cos \phi\). Alternatively, it is directly indicated in terms of active \((P[i])\) and reactive \((Q[i])\) powers, given by

\[
P[i] = \frac{3}{2} \left( V_{C_{d},d}^2 + V_{C_{q},q}^2 \right) \]
\[
Q[i] = \frac{3}{2} \left( V_{C_{d},d}^2 - V_{C_{d},q}^2 \right) \]
(6)
(7)

Since the voltage \(V_{C_{d},q}^2\) has to be forced to zero in order to stabilize the system at the rated frequency with zero phase delay, active and reactive powers can be decoupled and expressed only as functions of \(V_{C_{d}}\). Finally, having in mind a reactive-inductive load, it can be expressed as

\[
R[i] = \frac{P[i]}{(P[i])^2 + (Q[i])^2} \quad \text{and} \quad L[i] = \frac{Q[i]}{(P[i])^2 + (Q[i])^2}.
\]
(8)
(9)

**B. The current sharing control problem**

We are now in a position to introduce the current sharing control problem. Having in mind the train application, where loads can be dropped or added at any time instant depending on different needs, the objective is to continuously share the load among all the inverters. Assuming that the quadrature component \(V_{C_{q}}\) is steered to zero, we can formulate the following definition of “direct current load sharing” [11].

**Definition 1:** Direct current load sharing is achieved if the overall direct component of the load current is equally shared among the inverters, i.e.,

\[
I_{od} = \frac{1}{n} \sum_{i=1}^{n} I_{i,d}^* \quad I_{od} = \frac{1}{n} \sum_{i=1}^{n} I_{i,q}^* \quad I_{od} \in \mathbb{R} \quad (10)
\]

where \(I_{od}, I_{d} \in \mathbb{R}^n\) are the inverters’ output and load currents, while \(I_{n,i} \in \mathbb{R}^n\) is the vector containing all ones.

Hence, the control problem to solve is the following one.

**Control Problem 1:** Given system (3)–(5), design a control law such that \(V_{C_{d}}\) and \(V_{C_{q}}\) are regulated in a neighborhood of zero and \(V_{C_{d},d}\), respectively, thus enabling current sharing in spite of connection and disconnection of unknown bounded loads.

**IV. The Proposed Sliding Mode Based Droop Control Strategies**

In this section, the previous control problem is solved by introducing sliding mode based droop control algorithms. Inspired by [10], the so-called sliding variable is designed as a voltage-current droop function. Before introducing the controllers, the following assumption is also required.

**Assumption 2:** The voltage reference \(V_{C_{d},d}\) is of class \(C^2\) and with first time derivative Lipschitz continuous, while, \(V_{C_{d},q} = 0\).

**A. Design of the sliding manifold**

Considering system (3) separately for the direct and quadrature component, and assuming to have small line impedence, relying on the Thévenin’s theorem, it is possible to verify that in steady-state among parallel branches it holds

\[
I_{td}^* = \frac{R_{vd}}{R_{vd}^2 + \omega_0^2}, \quad i = 1, \ldots, n - 1 \quad (11)
\]

where \(R_{vd}, \nu \in \{d, q\}\) are the so-called virtual resistances of the inverters. Considering the references \(V_{C_{d},d}^*\) and \(V_{C_{d},q}^*\) equal for each inverter, the relationship (11) can be written also in terms of active and reactive powers. Hence, one can write the following droop strategy

\[
I_{td}^* = I_{td} - \frac{1}{R_{vd}} \left( V_{C_{d},d} - V_{C_{d}}^* \right) \\
I_{tq}^* = I_{tq} - \frac{1}{R_{vq}} \left( V_{C_{q},q} - V_{C_{q}}^* \right) \quad (12)
\]

where \(I_{td}^*\) and \(I_{tq}^*\) are the direct and quadrature current references while \(I_{td}, I_{tq}\) are the nominal values. Analyzing the droop relationships in (12), the direct component is responsible to adapt the value of the current reference relying on the droop characteristics, while the quadrature component plays the important role to maintain the frequency at the rated value. Furthermore, each inverter can be regulated without requiring information from the others.

Since the input of the \(i\)th inverter is given by the voltages \(V_{t_{d,i}}, V_{t_{q,i}}\) and the droop characteristics determine the current...
references, the sliding variables \( \sigma^{[i]} = [\sigma_d^{[i]}, \sigma_q^{[i]}] \) are designed as
\[
\begin{align*}
\sigma_d^{[i]} &= \frac{1}{R_c^{[i]}} \left( V_{C_i}^{[i]} - \ell_i \right) + \left( I_{0d}^i - I_{ld}^i \right) \\
\sigma_q^{[i]} &= \frac{1}{R_c^{[i]}} \left( V_{C_i}^{[i]} - \ell_i \right) + \left( I_{0q}^i - I_{lq}^i \right).
\end{align*}
\] (13)

Relying on system (3) and computing the first time derivative of (13), the corresponding relative degree (i.e., the minimum order of the time derivative \( \sigma^{(r)[i]} \) where the control input explicitly appears) is equal to 1. Hence, a first order sliding mode naturally applies. However, the main drawback of sliding mode control is the so-called chattering phenomenon. Among the possible solutions, HOSM control algorithms are the most successful techniques. In the following two different approaches are discussed, that is the Super-Twisting Sliding Mode (STSM) algorithm and the Suboptimal Second-Order Sliding Mode (SSOSM) control.

B. STSM control algorithm

One of the main features of STSM control is that it can be applied only to relative degree-one system. By virtue of the choice of the sliding variables (13), the considered first order system becomes
\[
\dot{\sigma}^{[i]} = f_1^{[i]}(x^{[i]}, d^{[i]}, w^{[i]}) + g u^{[i]}
\] (14)
where
\[
\begin{align*}
f_1^{[i]} &= \left[ \begin{array}{ccc} \frac{1}{R_c^{[i]} C_t} & \frac{1}{R_c^{[i]} C_t} & \frac{1}{R_c^{[i]} C_t} & -\frac{1}{R_c^{[i]} \ell_i} & -\frac{1}{R_c^{[i]} \ell_i} & -\frac{1}{R_c^{[i]} \ell_i} \\
\frac{1}{R_c^{[i]} C_t} & 0 & -\frac{1}{R_c^{[i]} C_t} & \frac{1}{R_c^{[i]} C_t} & -\frac{1}{R_c^{[i]} C_t} & \frac{1}{R_c^{[i]} C_t} \\
\frac{1}{R_c^{[i]} C_t} & -\frac{1}{R_c^{[i]} \ell_i} & \frac{1}{R_c^{[i]} \ell_i} & -\frac{1}{R_c^{[i]} \ell_i} & \frac{1}{R_c^{[i]} \ell_i} & -\frac{1}{R_c^{[i]} \ell_i}
\end{array} \right] \begin{bmatrix} x^{[i]} \\
d^{[i]} \end{bmatrix} \\
g &= \left[ \begin{array}{c} -\frac{1}{R_c^{[i]} C_t} \\
0 \\
0 \\
\frac{1}{R_c^{[i]} C_t}
\end{array} \right]
\] (15)
and \( u^{[i]} = \left[ \begin{array}{l} u_{d}^{[i]} \\
-u_{q}^{[i]}
\end{array} \right] \) are uncertain and bounded function such that the following assumption holds.

Assumption 3: There exist positive constants \( F_{1v}^{[i]}, F_{2v}^{[i]} \) and \( G_{1v \min}, G_{1v \max} \), with \( \nu \in \{d, q\} \), such that
\[
\begin{align*}
|f_1^{[i]}| &\leq F_{1v}^{[i]} \\
\left| \frac{df_1^{[i]}}{dt} \right| &\leq F_{2v}^{[i]} \\
-G_{1v \max} &\leq g_v \leq -G_{1v \min} < 0
\end{align*}
\] (17) (18) (19)
with \( f_1^{[i]} \) and \( g_v \) being the direct and quadrature components of (15) and (16), respectively.

The STSM control is instead given by
\[
\begin{align*}
\nu^{[i]} &= u_{d}^{[i]} + u_{q}^{[i]} \\
\nu_{d}^{[i]} &= \alpha_1^{[i]} |\sigma_d^{[i]}| sgn(\sigma_d^{[i]}) \\
\nu_{q}^{[i]} &= \alpha_2^{[i]} |\sigma_q^{[i]}| sgn(\sigma_q^{[i]})
\end{align*}
\] (20)
where \( \alpha_1^{[i]} \) and \( \alpha_2^{[i]} \) are positive constants chosen in order to dominate the uncertain terms affecting the system. Note that the control input directly fed into the plant is continuous, thus enabling a chattering alleviation property of the algorithm.

C. SSOSM control algorithm

Another possibility to solve the control problem at hand is the so-called SSOSM control algorithm. In this case, in order to achieve similar chattering alleviation property as for the STSM control, an artificially increment of the relative degree is operated. More specifically, introducing auxiliary variables \( \tilde{z}_{(j+1)}^{[i]} = \sigma^{(j)[i]} \), \( j = 0, 1 \), the so-called second-order auxiliary system results
\[
\begin{align*}
\tilde{z}_v^{[i]} &= \tilde{z}_v^{[i]} \\
\tilde{z}_{2v}^{[i]} &= f_{2v}^{[i]}(x^{[i]}, d^{[i]}, w^{[i]}) + g_v \mu_v^{[i]} 
\end{align*}
\] (21)
where \( f_{2v}^{[i]} = \frac{df_1^{[i]}}{dt} \) and \( g_v \) as in (16), which are bounded by virtue of Assumption 3. The so-called SSOSM control law is given by
\[
\mu_v^{[i]} = \gamma_v^{[i]} \alpha_3^{[i]} \text{sgn}(\tilde{z}_v^{[i]} - \frac{1}{2} z_{\max})
\] (22)
\[
\gamma_v^{[i]} \in (0, 1) \cap \left( 0, \frac{3G_{1v \min}}{G_{1v \max}} \right)
\] (23)
\[
\alpha_3^{[i]} > \max\left( \frac{F_{2v}^{[i]}}{R_0^{[i]} G_{1v \min}}, \frac{4F_{2v}^{[i]}}{G_{1v \min} - \gamma_v^{[i]} G_{1v \max}} \right)
\] (24)
where \( z_{\max} \) are the extremal values of \( z_v^{[i]} \), computed using a peak detector as discussed for instance in [18].

D. Convergence argumentation

Referring to [14, Chapter 2], and taking into account the features of the considered parallel multi-inverters system, one can prove that STSM and SSOSM controllers make the sliding variables, \( \sigma^{[i]} \), converge to zero in a finite time. On the other hand, the controlled voltages converge in a neighborhood of their reference values, which depends on the parameters of lines and loads, and on the selected virtual resistances. Furthermore, we also refer to [14] for providing some rules to practitioners interested in implementing the proposed sliding mode approaches.

V. Case Study

In this section the proposed HOSM control strategies are assessed on realistic simulator implemented in MATLAB–Sim Power Systems with data provided by Alstom for a train with 4 inverters and 7 loads.

A. Settings and scenario

The electric parameters of the system and the involved powers are reported in Table I. All the loads can be connected and disconnected by means of a dedicated switch and each VSC is fed by 600 V not-reversible voltage source. All the inverters are reasonably assumed to be equal. This assumption is not restrictive and, suitably tuning the control parameters, the proposal is valid even in case of heterogeneous subsystems. Inverters 1, 3 and 4 can be directly connected to their single loads \( \ell_1, \ell_3 \) and \( \ell_4 \), while inverter 2 can supply four loads \( \ell_{2j}, j = 1, 2, 3, 4 \) connected each other through three lines denoted as \( \ell_{2j+1} \). The first module is denoted as “master” inverter and is connected to the grid when the
corresponding capacitor voltage $V_{C_t}^{[1]}$ reaches a stable value around 400 V (initialization phase). All the other inverters are then connected shifted in time of 1 s. After 3.5 s, all inverters are turned on. At the time instant 5 s inverter 1 is turned off and activated after 1 s. Finally, from the time instant 8 s the loads are inserted according to the sequence reported in Table II. As for the controllers, the virtual resistances and gains are reported in Table III. The total simulation lasts 20 s with sampling time equal to 5 μs.

**B. Results**

When the loads start to connect (8 s), the condition of “direct current load sharing” is enabled. Figure 2 shows the evolution of the output powers ($P_i^{[j]}$) of the inverters (left) and the three-phase voltages (right) only on the 1st capacitor ($v_{C_t}^{[1]}$), as an example. The latter appears a smooth three-phase sinusoidal signal and at time 8 s the connection of load $t_1$ does not affect the voltages, thus assessing the robustness of the proposals in front of units connection and disconnection.

**TABLE I**

**INVERTERS, LINES AND LOADS PARAMETERS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{DC_{i}}$</td>
<td>600 V</td>
</tr>
<tr>
<td>$R_{i}$</td>
<td>1 mΩ</td>
</tr>
<tr>
<td>$L_i$</td>
<td>210 μH</td>
</tr>
<tr>
<td>$C_i$</td>
<td>2.4 mF</td>
</tr>
<tr>
<td>$\omega_0$</td>
<td>50 Hz</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Frequency</th>
<th>Switching Time</th>
<th>Resistance</th>
</tr>
</thead>
<tbody>
<tr>
<td>5 Hz</td>
<td>1 s</td>
<td>1.95 Ω</td>
</tr>
<tr>
<td>20 Hz</td>
<td>200 ms</td>
<td>1.95 Ω</td>
</tr>
</tbody>
</table>

**TABLE II**

**SCENARIO: INVERTERS INITIALIZATION PHASE (GRAY), INVERTERS/LOADS POWER ON (LIGHT GRAY) AND POWER OFF (DARK GRAY) INTERVALS**

<table>
<thead>
<tr>
<th>Scenario</th>
<th>Time (s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
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</tr>
<tr>
<td>1</td>
<td>2</td>
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<tr>
<td>2</td>
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<td>18</td>
<td>19</td>
</tr>
<tr>
<td>19</td>
<td>20</td>
</tr>
</tbody>
</table>

**TABLE III**

**CONTROL PARAMETERS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{C_t}^{d}$</td>
<td>326.6 V</td>
</tr>
<tr>
<td>$V_{C_t}^{q}$</td>
<td>0 V</td>
</tr>
<tr>
<td>$R_{i}$</td>
<td>$80 \times 10^{-6}$ Ω</td>
</tr>
</tbody>
</table>

| STSM | $\alpha_1$ | 1 |
|      | $\alpha_2$ | 1 |

| SSOSM | $\gamma_1$ | 1 |
|       | $\alpha_1$ | 1 |

**C. Comparison**

To further assess the proposals, they are compared with droop-control having Proportion-Integral regulation of the inverter currents (DC [10]) and with a First-Order Sliding Mode (FOSM) control. For a quantitative comparison the Total Harmonic Distortion (THD) of the load voltages, and the root-mean-square values of the dc-link voltages ($V_{DC_{rms}}$) are computed. For these indices, then the average among all the loads and inverters (namely $m(\cdot)$) is achieved and
when the load is supplied, the best performance over time is instead guaranteed by the SSOSM algorithm, which is coupled due to the presence of line impedances among the inverters, has reported in Table IV. As for the THD, the performance is similar for all the controllers apart from that of STSM. This aspect can be noticed, for instance, in Figure 3 (top) for load $\ell_{22}$. Indeed, in our tests, STSM is more sensitive to load variations, presenting greater voltage distortions than those visible when using the other methods. During the interval when the load is supplied, the best performance over time is still guaranteed by the SSOSM algorithm, which is always below the maximum allowed threshold (5%). As for the rms-values of the dc-link voltages, although they are similar for all the algorithms, in Figure 3 (bottom) the DC solution (black lines) is visibly more sensitive to connection and disconnection of inverters and loads (see, e.g., after the time instant 8 s).

VI. CONCLUSIONS

In this paper sliding mode based droop control strategies have been proposed for parallel-connected inverters in railway vehicles. The model of the system, which is coupled due to the presence of line impedances among the inverters, has been formulated. A STSM control and a SSOSM one have been designed and discussed. Finally, realistic simulation results, based on real data, have been illustrated and a comparative study with PI-based DC and FOSM control has been performed. Future works will attempt to introduce an adaptation mechanism to cope with eventually unknown bounds of the uncertain terms affecting the system.

TABLE IV

<table>
<thead>
<tr>
<th>Performance Indices</th>
<th>$m(\text{THD})$ [%]</th>
<th>$m(V_{\text{DC rms}})$ [V]</th>
</tr>
</thead>
<tbody>
<tr>
<td>DC [10]</td>
<td>2.032</td>
<td>599.26</td>
</tr>
<tr>
<td>FOSM</td>
<td>2.036</td>
<td>599.06</td>
</tr>
<tr>
<td>STSM</td>
<td>4.47</td>
<td>599.09</td>
</tr>
<tr>
<td>SSOSM</td>
<td>2.046</td>
<td>599.01</td>
</tr>
</tbody>
</table>

Fig. 3. Total Harmonic Distortion (THD) of the load voltage (top) when $\ell_{22}$ is connected, and the dc-link voltage $V_{\text{DC2}}$ (bottom)

REFERENCES